Performance and Measurement of Current Control Schemes in Grid connected Inverters

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ABSTRACT

Background: Applications such as digital based control in grid connected output currents are measured accurately. So obtaining the accuracy must in the design consideration. The proposed work analyze the single and three phase measurement of grid connected inverter and the current control strategy is viewed.

INTRODUCTION

Single-phase Grid-connected Inverters:

Fig. 1 presents the block diagram of the current control in consideration of the measurement offsets of the utility voltage and output current in single-phase grid-connected inverters. In Fig. 1, \( \delta v_g \) and \( \delta i_g \) denote the measurement offsets in the utility voltage and the inverter output current, respectively, whereas \( v_{g,m} \) and \( i_{g,m} \) denote the measured utility voltage and inverter output current, respectively. On the assumption that the utility voltage is a complete sinusoidal wave as expressed in Equation (1), the inverter output current reference is expressed as Equation (2) so that active and reactive power may be regulated at the utility side, the pulse width modulation (PWM) is ideal, and the measurement offsets are constant. The inverter output current at the steady state in the current control using a proportional-integral-resonant (PIR) regulator (as expressed in Equation (3)) is expressed in Equation (4).

\[
\begin{align*}
v_g &= v_g^* \sin \alpha \\
i_g^* &= I_g^* \sin \alpha + I_q^* \cos \alpha
\end{align*}
\]

Fig. 1: Block diagram of the current control in measurement in single-phase grid-connected inverters.

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As Equation (4) shows, the current measurement offset causes the DC current component injected into the utility side in single-phase grid-connected inverters. At the steady state, using Equations (1) and (4) yields the inverter output voltage as follows:

\[ v_{inv} = L_i \frac{di}{dt} + v_g = \omega L_i I_d^* \cos \omega t - \omega L_i I_q^* \sin \omega t + V_g \sin \omega t \]  

Equation (5)

If no power loss occurs in the inverter, the power supplied from the DC-link to the inverter is similar to the inverter output power and can be expressed as Equation (6).

\[ P_{out \_inv} = -P_S \{ \delta t_i \sin (\omega t + \phi_1) \} \]  

Equation (6)

If the DC-link capacitor voltage is maintained at the constant operating point \( V_{dc^*} \), the power absorbed in the DC-link capacitor can be obtained as follows:

\[ P_{dc} = \frac{C_{dc} \delta \dot{V}_{dc}}{2} = P_S \left\{ \delta t_i \sin (\omega t + \phi_2) + \frac{I_d^*}{2} \cos (\omega t + \phi_2) - \frac{I_q^*}{2} \sin (\omega t + \phi_2) \right\} \]  

Equation (7)

Equation (7) shows that the current measurement offset causes the DC-link capacitor voltage to oscillate at the utility voltage frequency in single-phase grid-connected inverters. This ripple degrades the overall system performance because it generates inverter output current reference with harmonics; thus, the real inverter output current also has harmonic components.

**Three-phase Grid-connected Inverters:**

Fig. 3 presents the block diagram of the current control in consideration of the measurement offsets in three-phase grid-connected inverters. In Fig. 3, the stationary dq-axis measurement offsets of utility voltages and inverter output current, respectively, whereas the stationary dq-axis measurement values of utility voltages and inverter output current, respectively. If the utility phase voltages are sinusoidal-balanced voltages, the stationary dq-axis voltages are expressed as Equation (8). The stationary dq-axis reference values of the inverter output phase current can be expressed as Equation (9) to control the active and reactive power at the utility side. Moreover, if the space vector pulse width modulation is ideal and the measurement offsets are constant, the stationary dq-axis output currents at the steady state in the current control system using a PIR regulator are expressed as Equation (10).

\[ e_d^* = E \cos \omega t \]  

\[ e_q^* = E \sin \omega t \]  

\[ i_d^* = I_d^* \cos \omega t - I_q^* \sin \omega t \]  

\[ i_q^* = I_d^* \sin \omega t + I_q^* \cos \omega t \]  

\[ i_d = I_d^* \cos \omega t - I_q^* \sin \omega t - \delta t_d \]  

\[ i_q = I_d^* \sin \omega t + I_q^* \cos \omega t - \delta t_q \]  

Equation (8)
Equation (10) shows that the current measurement offsets cause DC current components injected into the utility side in three-phase grid-connected inverters. At the steady state, using Equations (8) and (10) yields stationary dq-axis inverter output voltages as follows:

\[ v_d^s = L_i \frac{di_d^s}{dt} + e_d^s \]
\[ = -\omega L_i f_d^s \sin \omega t - \omega L_i f_q^s \cos \omega t + E \cos \omega t \]

Equation (11)

**Fig. 3**: Block diagram of the current control in measurement of three-phase grid-connected inverters.

If no power loss occurs in the inverter, the power supplied from the DC-link to the inverter is similar to the inverter output power and can be expressed as Equation (12).

\[ P_{out \_dc} = \frac{3}{2} E f_d^s \]

Equation (12)

If the DC-link capacitor voltage is maintained at the constant operating point \( (V_{dc^*}) \), the power absorbed in the DC-link capacitor can be obtained as follows:

\[ P_{dc} = \frac{C_{dc}}{2} \frac{dv_{dc}^2}{dt} \]
\[ = \frac{3}{2} P_{dc} \left\{ \delta i_d^s \cos (\omega t + \phi_d) + \delta i_q^s \sin (\omega t + \phi_q) \right\} \]

Equation (13)

Equation (13) shows that current measurement offsets cause the DC-link capacitor voltage to oscillate at the utility voltage frequency in three-phase grid-connected inverters. This ripple deteriorates the overall system performance because it generates inverter output current references with harmonics. Thus, real inverter output currents also have harmonic components.

**Design Consideration:**
**Current Measuring Single-phase Grid-connected Inverters:**

From Equation (7), the equation for the ripple component of the DC-link capacitor voltage can be calculated as follows:

\[ v_{dc \_rip}^2 = v_{dc}^2 - v_{dc}^2 = \frac{2}{C_{dc}} \frac{1}{\omega} P_{dc} dt \]
\[ = \frac{2P_{dc}}{\omega C_{dc}} \left\{ -\delta i_d^s \cos (\omega t + \phi_d) \right\} \]
\[ + \frac{2P_{dc}}{\omega C_{dc}} \left\{ \frac{f_d^s}{4} \sin (2\omega t + \phi_d) + \frac{f_q^s}{4} \cos (2\omega t + \phi_q) \right\} \]

Equation (14)
Equation (14) can be approximated as Equation (15) because the magnitude of the utility voltage is much larger than the impedance drop

$$v_{dc-rip}^2 = \frac{2V_s^2}{\omega C_{dc}} \left\{ -\delta i_s \cos \omega t + \frac{I_d^e}{4} \sin 2\omega t + \frac{V_s^e}{4} \cos 2\omega t \right\}$$

(15)

As shown in Equation (15), the second-order ripple component exists in single-phase grid-connected inverters. The band pass filter expressed in Equation (16) is used to detect the first-order ripple component included in the current measurement offset.

$$T_{BPF}(s) = \frac{BW_s}{s^2 + BW_s^2 + \omega_{BPF}^2}$$

(16)

where $BW$ and $\omega_{BPF}$ are the bandwidth and the center frequency of the band pass filter, respectively. If $\omega_{BPF}$ is similar to the utility frequency, the first-order ripple component can be obtained with

$$x_q = \frac{2V_s}{\omega C_{dc}} \delta i_s \cos \omega t .$$

(17)

The first-order ripple component expressed in Equation (17) is then filtered through the all-pass filter expressed in Equation (18). When $\omega_{APF}$ is equal to the utility frequency, the all-pass filter generates a virtual component expressed as Equation (19), which leads the first-order ripple component by 90°.

$$T_{APF}(s) = \frac{s - \omega_{APF}}{s + \omega_{APF}}$$

(18)

$$x_q = \frac{2V_s}{\omega C_{dc}} \delta i_s \sin \omega t$$

(19)

By substituting Equations (17) and (19) into Equation (20), the magnitude of the first-order ripple component can be established as Equation (21).

$$m = \begin{bmatrix} \sin \omega t & -\cos \omega t \\ \end{bmatrix} \begin{bmatrix} x_d \\ x_q \end{bmatrix}$$

$$m = \frac{2V_s}{\omega C_{dc}} \delta i_s$$

(20)

(21)

The current measurement offset can be calculated with Equation (21), but the real value cannot be determined without exact parameter values. Moreover, the current measurement offset may be altered by external factors. Hence, a current offset controller is used to maintain the calculated first-order component at zero (Fig. 4). Fig. 5 presents the block diagram of a current control system using the proposed current offset compensator in single-phase grid-connected inverters. The current offset compensator is mainly composed of the first-order ripple component detector, the magnitude detector, and the current offset controller. In Fig. 5, $io\_com$ is a compensation value for the current measurement offset.

![current offset controller](image)

**Fig. 4:** Block diagram of a current offset controller in single-phase grid-connected inverters.
Fig. 5: Block diagram of a current control system using the proposed current offset compensator in single-phase grid-connected inverters.

Current Measuring Three-phase Grid-connected Inverters:

From Equation (13), the equation for the ripple component of the DC-link capacitor voltage can be calculated as follows:

\[
\frac{v_{dc}^2}{v_{dc}^2} - \frac{v_{dc}^2}{v_{dc}^2} = \frac{2}{C_{dc}} \int_{0}^{t} P_{dc}dt
\]

\[
= \frac{3P_{dc}}{C_{dc}} \left( \delta i_{d}^{p} \sin(\omega t + \phi_{d}) - \delta i_{q}^{p} \cos(\omega t + \phi_{d}) \right)
\]

Equation (22)

Equation (22) can be approximated as Equation (23) because the magnitude of the utility voltage is much larger than the impedance drop.

\[
x_{q} = \frac{3E}{C_{dc}} \left( \delta i_{d}^{p} \sin(\omega t) - \delta i_{q}^{p} \cos(\omega t) \right)
\]

Equation (23)

Using the all-pass filter expressed in Equation (18), the virtual component that leads the first-order ripple component by 90° can be obtained as follows:

\[
x_{d} = \frac{3E}{C_{dc}} \left( \delta i_{d}^{p} \cos(\omega t) + \delta i_{q}^{p} \sin(\omega t) \right)
\]

Equation (24)

The magnitude of the d-axis and q-axis voltage ripple can be obtained with Equation (26) by substituting Equations (23) and (24) into Equation (25).

\[
\begin{bmatrix}
m_{d} \\
m_{q}
\end{bmatrix} =
\begin{bmatrix}
\cos(\omega t) & \sin(\omega t) \\
\sin(\omega t) & -\cos(\omega t)
\end{bmatrix}
\begin{bmatrix}
x_{d} \\
x_{q}
\end{bmatrix}
\]

Equation (25)
Similar to single-phase grid-connected inverters, using a current offset controller (Fig. 6) is necessary. Fig. 7 presents the block diagram of a current control system using the proposed current offset compensator in three-phase grid-connected inverters. In Fig 7, stationary dq-axis compensation values for current measurement offsets are represented.

Fig. 8: Sample distributed generation system for simulations and experiments.

**Simulation Results:**

Simulations and experiments were conducted with a sample distributed generation system (Fig. 8) to investigate

Fig. 9: Output of the current offset compensator in single-phase, grid-connected inverters.
Simulations and experiments were conducted with a sample distributed generation system (Fig. 8) to investigate. In the case of single-phase grid-connected inverters; a second-order voltage ripple that distorts the inverter output current inevitably exists in the DC-link capacitor voltage. A notch filter whose centre frequency is twice the utility frequency was utilized to eliminate the effect of the ripple component. Fig. 9 shows an output of the current offset. Fig. 9 shows an output of the current offset compensator in single-phase grid-connected inverters. In the figure, the output of the current offset compensator converges at 1 A at the steady state to eliminate the effect of the current measurement offset. Fig. 10 shows the output of the current offset compensator in three-phase grid-connected inverters. Similar to single-phase grid-connected inverters, the current offset compensator calculates the compensation values at the steady state. The effect of the current measurement offset is thus eliminated.

![Fig. 10: Output of the current offset compensator in three-phase grid-connected inverters](image)

**Conclusion:**

These single and three phase current control measurement grid inverter is used as the grid interface of a higher power rated conversion system, since it has only a single and three phase dc source. The proposed fixed current control leads to an equal and uniform distribution of the switching stress among the various switches. It is shown that following the proposed gain calculation method ensures the operation of the circuits which is fixed frequency of the carrier. With the multicarrier level shifted current control, the net switching frequency increases and the ripple magnitude is reduced leading to a higher feed forward gain and hence better control characteristics. It is shown through simulation results that the available measurement can be controlled to feed the load real power with the balance real power being supplied from the grid. In addition to real power injection, the objective of load compensation is also achieved leading to a balanced, distortion free, and unity power factor source current. The voltages across the operation are balanced under all conditions with the proposed control method.

**REFERENCES**


