

Relation Between Thickness and k-PCB Model

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Abstract: The thickness of a graph is the minimum number k such that G is the union of k planar graphs. Determining the thickness of a given graph is known to be an NP-complete problem [1, 2, 3], in this article we show relation between thickness and K-PCB model and an open problem for this model.

Key words: thickness, K-PCB model

INTRODUCTION

The thickness problem, asking for the thickness of a given graph G , is NP-hard, so there is little hope to find a polynomial-time algorithm for the thickness problem for general graphs. However, for some graph classes, the thickness can be determined in polynomial time. For example, the thickness is known for complete and complete bipartite graphs, the thickness problem has application in VLSI design. In electronic circuits, components are joined by means of conducting strips. These may not cross, since this would lead to undesirable signals in this case, an isolated wire must be used. For that reason, circuits with a large number of crossings are decomposed into several layers without crossing, which are then pasted together. The goal is to use as few layers as possible. In this application it would be desirable to know the thickness of a hypercube whose nodes are cells to be placed and whose hyper edges correspond to the nets connecting the cells. If the thickness problem could be solved for graphs, it would be a useful engineering tool in the layout of electronic circuits. In this article we show relation between thickness and K-PCB model and an open problem for this model.

2. Characterization of Planar Graphs:

Def (2.1) A graph H is said to be homeomorphic from G if either $H \cong G$ or H is isomorphic to a subdivision of G . A graph G_1 is homeomorphic with G_2 if there exists a graph G_3 such that G_1 and G_2 are both homeomorphic from G_3 .

Theorem (2.1) [A] A graph is planar if and only if it does not contain a sub graph which is homeomorphic to K_5 or $K_{3,3}$.

Theorem (2.2) [5] G be a connected planar graph with n vertices, m edges, and f faces, then we have for the plane embedding of G that $n - m + f = 2$

Corollary (2.2) [1] If $L(f_i)$ denotes the length of face f_i in a planar graph G , then

$$\sum L(f_i) = 2E$$

Theorem (2.3) If G is a simple planar graph with at least three vertices, then $E(G) \leq 3n - 6$. If also G is triangle-free, then $E(G) \leq 2n - 4$.

Proof. It suffices to consider connected graphs, otherwise we could add edges. Euler's formula will relate n and E if we can dispose of f . Theorem (2.2) provides an equality between E and f . Every face boundary in a simple planar graph contains at least three edges (if $n \geq 3$). Letting $\{f_i\}$ be the list of face lengths, this yields $2E = \sum f_i \geq 3f$

$$\text{But } n - E + f = 2 \rightarrow f = 2 - n + E \rightarrow 2E \geq 6 - 3n - 3E \rightarrow E \leq 3n - 6$$

When G is triangle-free, the faces have length at least 4.

$$\text{In this case } 2E = \sum f_i \geq 4f$$

$$\text{But } n - E + f = 2 \rightarrow f = 2 - n + E \rightarrow 2E \geq 8 - 4n + 4E \rightarrow E \leq 2n - 4$$

3. Thickness:

Def (3.1) The thickness of a graph is the minimum number K such that G is the union of K planar graphs.

Here, by "union of K planar graphs" we mean that the edges of G can be partitioned into K sets so that the graph induced by each set is planar.

Theorem (3.1) If $G = (V, E)$ is a graph with $(|V| = n > 2)$ and $|E| = m$, then:

- i) $\theta(G) \geq \lceil \frac{m}{3n-6} \rceil$
- ii) $\theta(G) \geq \lceil \frac{m}{2n-4} \rceil$, if G has no triangle.

Proof. by theorem (2.3), the denomination is the maximum size of each planar sub graph. The pigeonhole principle then yields the inequality.

Corollary (3.1) [7] the thickness of the complete graph K_n is

$$\theta(K_n) = \lceil \frac{n+7}{6} \rceil, \text{ for } n=9,10 \quad \theta(K_9) = \theta(K_{10}) = 3$$

Corollary (3.2) [8] the thickness of the complete bipartite graph $K_{m,n}$ is

$$\theta(K_{m,n}) = \lceil \frac{mn}{2(m+n-2)} \rceil \text{ except if } m \text{ and } n \text{ are both odd, } m \leq n \text{ and there is an integer } k \text{ satisfying } n = \lceil \frac{2k(m-2)}{m-2k} \rceil$$

Theorem (3.3) [7] the thickness of the complete bipartite graph $K_{n,n}$ is $\theta(K_{n,n}) = \lceil \frac{n+5}{4} \rceil$

Theorem (3.4) [10, 11, 12] if $G=(V, E)$ is a graph with m adage and maximal degree d . then

- i) $\theta(G) \leq \lfloor \sqrt{\frac{m}{3}} + \frac{7}{6} \rfloor$
- ii) $\theta(G) \leq \lfloor \frac{d}{2} \rfloor$

4. Embeddings for VLSI:

An "embedded adage" of G consists of a path of horizontal an/or vertical segments with the restriction that the only intersection allowed among these results are those in which the horizontal segment of an embedded edge intersects the vertical segment of another embedded edge or vice versa.

An embedded if a graph G in a One- layer grid is a mapping of the nodes of G to paths in the Grid [6, 9]. The paths repressing edge are disjoint except possibly at their end points. An example is shown in figure (4.1).

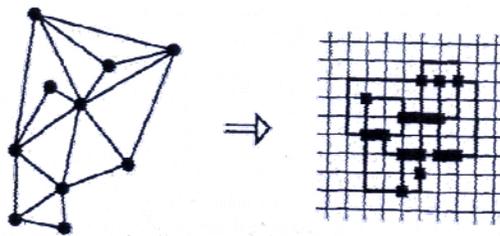


Fig. 4.1: A grid embedding of a graph.

(4.1) K-PCB Model:

This model consists of K grid layer with each mode of G embedded in the same position an each layer, and edge embedded as paths in the grid which may change layers at contact cuts.

An edge path may begin and end on any layer. But, as before, within each layer the paths must not intersect each other except possibly are their endpoints. (PCB stands for printed circuit board)/ The K -PCB model corresponds to K -layer printed circuit boards in which points of a mounted chip are present on all layers. Figure (4.2) shows an embedding of K_5 in the 2-PCB model.

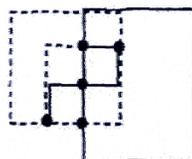


Fig. 4.2: 2-PCB embedding of K_5

4.2. Relation Between Thickness and K-PCB Model:

A graph can be embedded in the K-PCB model without contact cuts if and only if it has thickness at most K.

Proof. It is clear that the number of layers needed to embed a graph G is at least its thickness, and a simple homotopy argument proves the opposite direction.

5. Conclusion:

In this paper we present result have concerning the thickness and K-PCB model. In particular for every K-PCB model in some sense free contact cuts for each edge at its endpoint since each edge can connect to its endpoint nodes on any layer.

6. Open problem:

Whether the use of K-PCB model can applied complete bipartite $k_{3,3}$?

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